ELECTRONICALLY FILED

Attorney Docket No. 1008-743.301

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application)	PATENT APPLICATION
)	
Inventor(s): Donald Paul Richmond II)	
)	Art Unit: 2829
Application No.: 10/718,825)	
)	Examiner: Arleen M. Vazquez
Filed: November 11, 2003)	
)	Confirmation No.: 1336
Title: Wafer Level Burn-In and Electrical)	
Test System and Method)	
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Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

In response to the Office Action mailed March 25, 2008, please amend the above-identified application as follows:

Listing of Claims begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.